

PATENT NUMBER

U.S. **UTILITY** Patent Application

**O.I.P.E.**

PATENT DATE

SCANNED

**Q.A**

APPLICATION NO. 09/631623	CONT/PRIOR F	CLASS 438 : 7	SUBCLASS C. 7	ART UNIT 2812	EXAMINER K
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## APPLICANTS

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**TITLE**

Semiconductor device having a potential fuse, and method of manufacturing the same

PTO-2040  
12/99

**ISSUING CLASSIFICATION**

<b>ORIGINAL</b>						<b>CROSS REFERENCE(S)</b>						
<b>CLASS</b>			<b>SUBCLASS</b>			<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>					
								.				
<b>INTERNATIONAL CLASSIFICATION</b>												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
___ The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
___ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
___ The terminal ___ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	
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